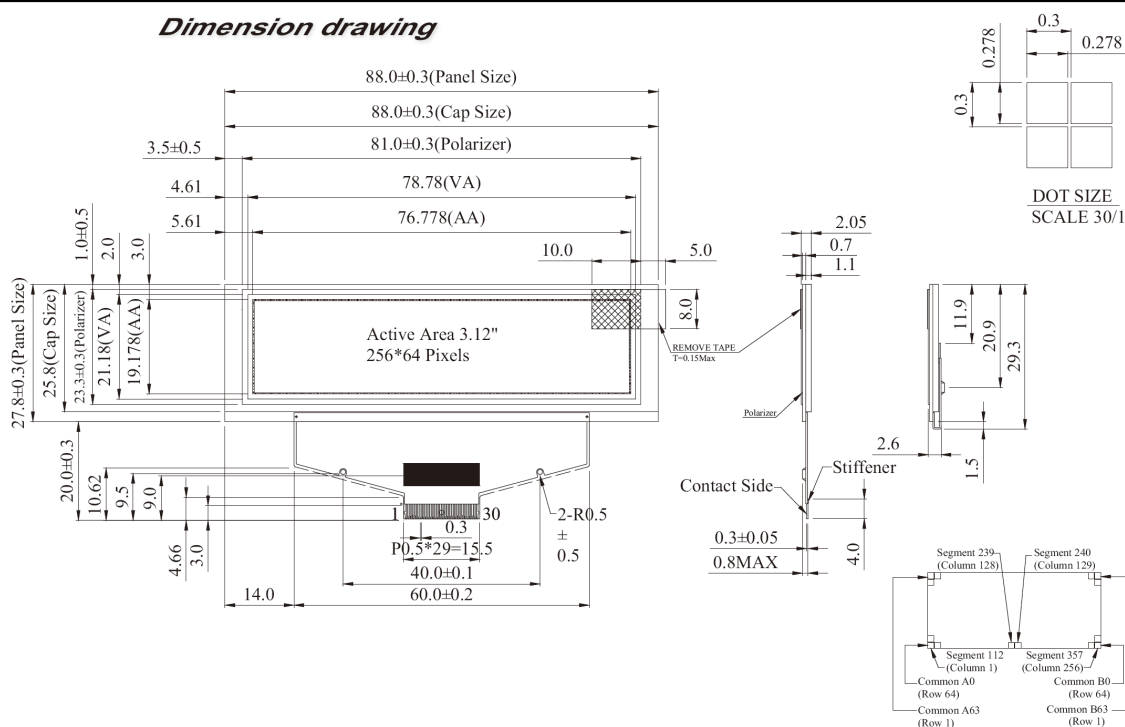


Dimension drawing



Feature

1. 256x64 dots
2. Built-in Controller SSD1322U
3. +3V power supply
4. 1/64 duty cycle
5. Interface: 6800, 8080, SPI
6. Polarizer optional

Mechanical Date

Item	Dimension	Unit
Module dimension	88.0 × 27.8 × 2.6	mm
View area	78.78 × 21.18	mm
Active area	76.778 × 19.178	mm
Dot Size	0.278 × 0.278	mm
Dot Pitch	0.3 × 0.3	mm

Absolute Maximum Rating

Parameter	Symbol	Min	Max	Unit	Notes
Supply Voltage for Logic	VDD	-0.3	4	V	1, 2
Supply Voltage for Display	VCC	0	16	V	1, 2

Electronical Characteristics

Characteristics	Symbol	Conditions	Min	Typ	Max	Unit
Supply Voltage for Operation	V _{CI}		2.4	2.8	3.5	V
Supply Voltage for Logic	V _{DD}		2.4	2.5	2.6	V
Supply Voltage for I/O Pins	V _{DDIO}		1.65	1.8	V _{CI}	V
Supply Voltage for Display	V _{CC}	Note 3	11.5	12	12.5	V
High Level Input	V _{IH}		0.8×V _{DDIO}	-	V _{DDIO}	V
Low Level Input	V _{IL}		0	-	0.2×V _{DDIO}	V
High Level Output	V _{OH}	I _{OUT} = 100µA, 3.3MHz	0.9×V _{DDIO}	-	V _{DDIO}	V
Low Level Output	V _{OL}	I _{OUT} = 100µA, 3.3MHz	0	0	0.1×V _{DDIO}	V
Operating Current for V _{CI}	I _{CI}	Note 4 Note 5	--	1.8 1.8	2.25 2.25	mA
Operating Current for V _{CC}	I _{CC}	Note 4 Note 5	--	26.3 41.1	32.9 51.4	mA
Sleep Mode Current for V _{CI}	I _{CC, SLEEP}		-	1	5	µA
Sleep Mode Current for V _{CC}	I _{CC, SLEEP}		-	1	5	µA

Note 3: Brightness (L_{br}) and Supply Voltage for Display (V_{CC}) are subject to the change of the panel characteristics and the customer's request.
 Note 4: V_{CI} = 2.8V, V_{CC} = 12V, 50% Display Area Turn on.
 Note 5: V_{CI} = 2.8V, V_{CC} = 12V, 100% Display Area Turn on.

Pin No.	Symbol	Function		
1	NC(GDN)	Reserved Pin (Supporting Pin)		
2	VSS	Ground of Logic Circuit		
3	VCC	Power Supply for OEL Panel		
4	VCOMH	Voltage Output High Level for COM Signal		
5	VLSS	Ground of Analog Circuit		
6~13	D7~D0	Host Data Input/Output Bus		
14	E/RD#	Read/Write Enable or Read		
15	R/W#	Read/Write Select or Write		
16	BS0	Communicating Protocol Select		
17	BS1	These pins are MCU interface selection input. See the following table:		
			BS0	BS1
		3-wire SPI	1	0
		4-wire SPI	0	0
		8-bit 68XX Parallel	1	1
8-bit 80XX Parallel	0	1		
18	DC#	Data/Command Control		
19	CS#	Chip Select		
20	RES#	Power Reset for Controller and Driver		
21	FR	Frame Frequency Triggering Signal		
22	IREF	Current Reference for Brightness Adjustment		
23	NC	Reserved Pin		
24	VDDIO	Power Supply for I/O Pin		
25	VDD	Power Supply for Core Logic Circuit		
26	VCI	Power Supply for Operation		
27	VSL	Voltage Output Low Level for SEG Signal		
28	VLSS	Ground of Analog Circuit		
29	VCC	Power Supply for OEL Panel		
30	NC(GND)	Reserved Pin (Supporting Pin)		