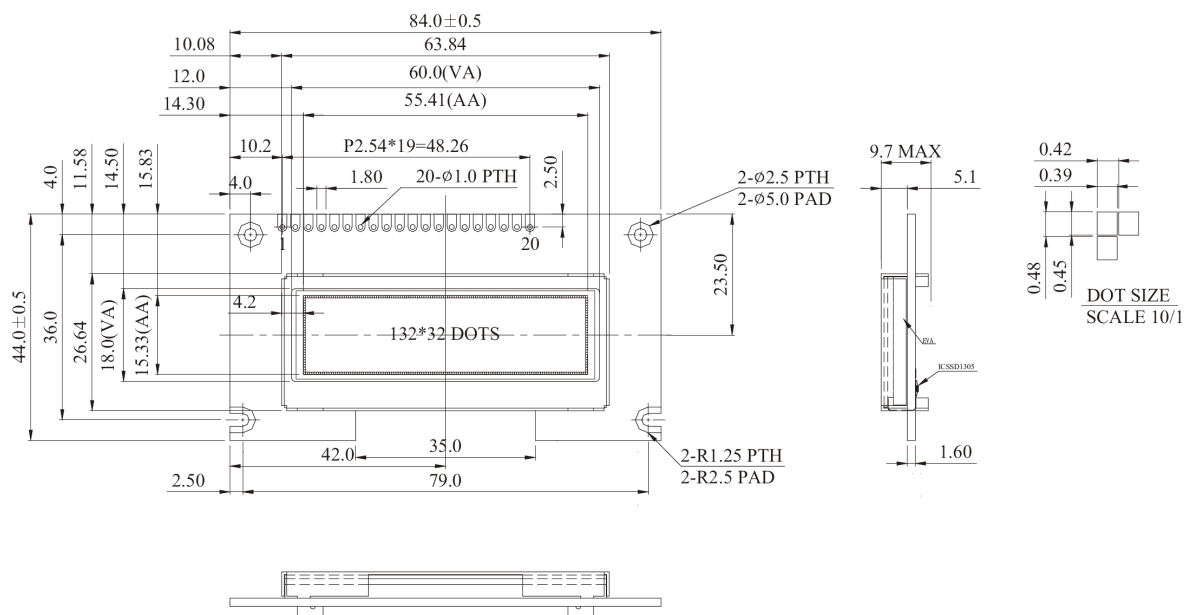


Dimension drawing



Feature

1. 132x32 dots
2. Built-in Controller SSD1305T7R1
3. +3V power supply
4. 1/32 duty cycle
5. Interface: 6800, 8080 & SPI & I2C optional
6. Sunlight readable & polarizer optional

Mechanical Date

Item	Dimension	Unit
Module dimension	84.0 x 44.0 x 9.7(MAX)	mm
View area	60.0 x 18.0	mm
Active area	55.41 x 15.33	mm
Mounting hole	79.0 x 36.0	mm
Dot size	0.39 x 0.45	mm
Dot pitch	0.42 x 0.48	mm

Pin No.	Symbol	Function
1	VSS	Ground.
2	VDD	Power supply for analog circuit.
3	NC	No connection
4	D/C	Data/ Command control. Pull high for write/read display data. Pull low for write command or read status.
5	CS	Chip select input.
6	NC	No connection
7	NC	No connection
8	E	Data read operation is initiated when it's pull low.
9	R/W	Data write operation is initiated when it's pull low.
10~17	DB0~DB7	Data bus.
18	RES	Reset signal input. When it's low, initialization of SSD1305 is executed.
19	DISP	Display off
20	NC	No connection

Absolute Maximum Rating

Parameter	Symbol	Min	Max	Unit	Notes
Supply Voltage for Logic	VDD	-0.3	3.5	V	1, 2
Supply Voltage for Display	VCC	8	16	V	1, 2

Electronical Characteristics

Characteristics	Symbol	Conditions	Min	Typ	Max	Unit
Supply Voltage for Logic	VDD	—	2.4	2.7	3.5	V
Supply Voltage for Display	VCC	—	11.0	13.0	15.0	V
High Level Input	V _{IH}	I _{OUT} = 100µA, 3.3MHz	0.8×VDD	—	VDD	V
Low Level Input	V _{IL}	I _{OUT} = 100µA, 3.3MHz	0	—	0.2×VDD	V
High Level Output	V _{OH}	I _{OUT} = 100µA, 3.3MHz	0.9×VDD	—	VDD	V
Low Level Input	V _{OL}	I _{OUT} = 100µA, 3.3MHz	0	—	0.1×VDD	V
Operating Current for VDD	I _{DD}	Note 4	—	30	—	mA
Sleep Mode Current for VDD	I _{DD, SLEEP}	—	—	—	1.5	mA

Note 3: Brightness (Lbr) and Supply Voltage for Display (VCC) are subject to the change of the panel characteristics and the customer's request.
Note 4: VDD = 2.7V, VCC = 13V, 50% Display Area Turn on.