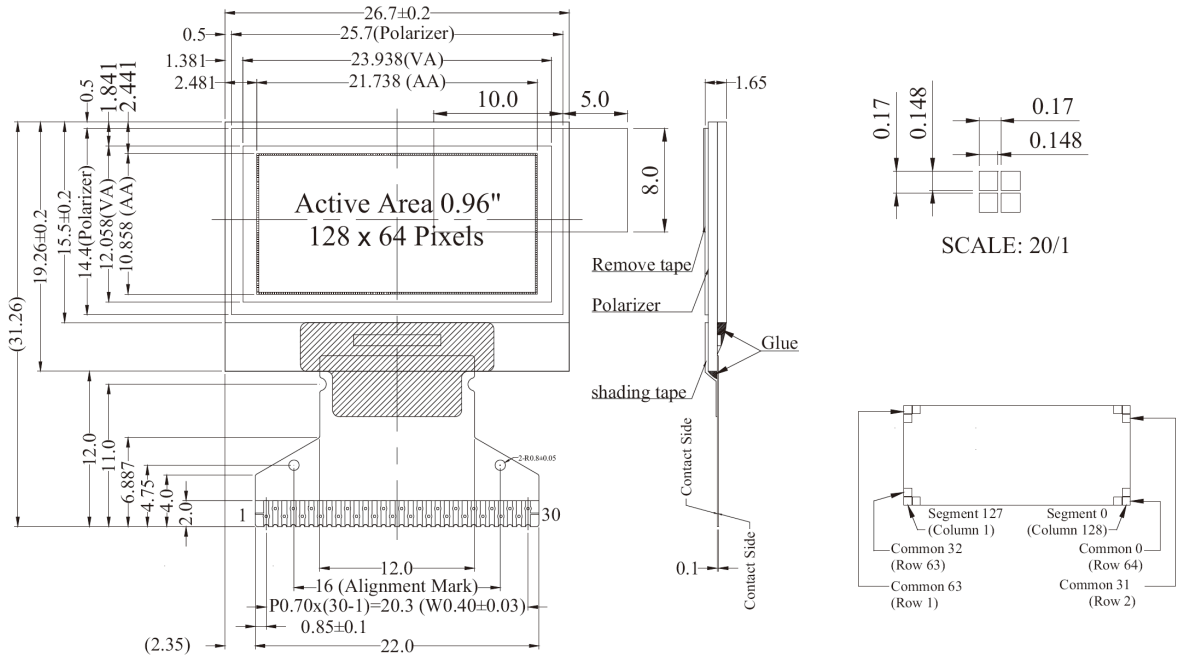
MEO012864D
OLED Graphic 128x64

GMS. DISPLAY SYSTEMS

Dimension drawing



Feature

1. 128x64dots
2. Built-in Driver SSD1306Z
3. +3V power supply
4. 1/64 duty cycle
5. Interface: 6800, 8080, SPI, I2C
6. polarizer optional

Pin NO.	Symbol	Description
1	N.C. (GND)	Reserved Pin (Supporting Pin)
2	C2P	Positive Terminal of the Flying Inverting Capacitorr
3	C2N	Negative Terminal of the Flying Boost Capacitor
4	C1P	The charge-pump capacitors are required between the terminals. They must be floated when the converter is not used.
5	C1N	
6	VBAT	Power Supply for DC/DC Converter Circuit
7	NC	NC
8	VSS	Ground of Logic Circuit
9	VDD	Power Supply for Logic
10	BS0	Communicating Protocol Select
11	BS1	These pins are MCU interface selection input. See the following table:
12	BS2	

	BS0	BS1	BS2
I2C	0	1	0
3-wire SPI	1	0	0
4-wire SPI	0	0	0
8-bit 68XX Parallel	0	0	0
8-bit 80XX Parallel	0	1	1

13	CS#	Chip Select
14	RES#	Power Reset for Controller and Driver
15	D/C#	Data/Command Control
16	R/W#	Read/Write Select or Write
17	E/RD#	Read/Write Enable or Read
18~25	D0~D7	Host Data Input/Output Bus

Mechanical Date

Item	Dimension	Unit
Module dimension	26.7× 19.26 × 1.65	mm
View area	23.938 × 12.058	mm
Active area	21.738 × 10.858	mm
Dot Size	0.148 × 0.148	mm
Dot Pitch	0.17 × 0.17	mm

Absolute Maximum Rating

Parameter	Symbol	Min	Max	Unit	Notes
Supply Voltage for Logic	VDD	-0.3	4	V	1, 2
Supply Voltage for Display	VCC	0	16	V	1, 2

Electronical Characteristics

Characteristics	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage for Logic	VDD	–	2.4	2.7	3.5	V
Supply Voltage for Display	VCC	–	14.5	15	15.5	V
High Level Input	VIH	Iout = 100μA,3.3MHz	0.8×VDD	–	VDD	V
Low Level Input	VIL	Iout = 100μA,3.3MHz	0	–	0.2×VDD	V
High Level Output	VOH	Iout =100μA,3.3MHZ	0.9×VDD	–	VDD	V
Low Level Input	VOL	Iout =100μA,3.3MHZ	0	–	0.1×VDD	V
Operating Current for VDD	IDD	Note 4	–	250	400	μA
		Note 5	–	250	400	μA
Operating Current for VCC	ICC	Note 4	–	31	39	mA
		Note 5	–	53	66	mA
Sleep Mode Current for VDD	IDD, SLEEP		–	–	10	μA
Sleep Mode Current for VCC	ICC, SLEEP		–	–	10	μA

Note 3: Brightness (L_{BR}) and Supply Voltage for Display (V_{CC}) are subject to the change of the panel characteristics and the customer's request.
Note 4: V_{DD} = 2.7V, V_{CC} = 15V, 50% Display Area Turn on.
Note 5: V_{DD} = 2.7V, V = 15V, 100% Display Area Turn on.