

DLC Display Co., Limited

德爾西顯示器有限公司



MODEL No:DLC0390BZR

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Record of Revision

Date	Revision No.	Summary
2017-05-29	1.0	Rev 1.0 was issued

1. Scope

This data sheet is to introduce the specification of DLC0390BZR active matrix TFT module. It is composed of a color TFT-LCD panel, driver IC, FPC and a backlight unit. The 3.9" display area contains 480x(RGB)x128 pixels.

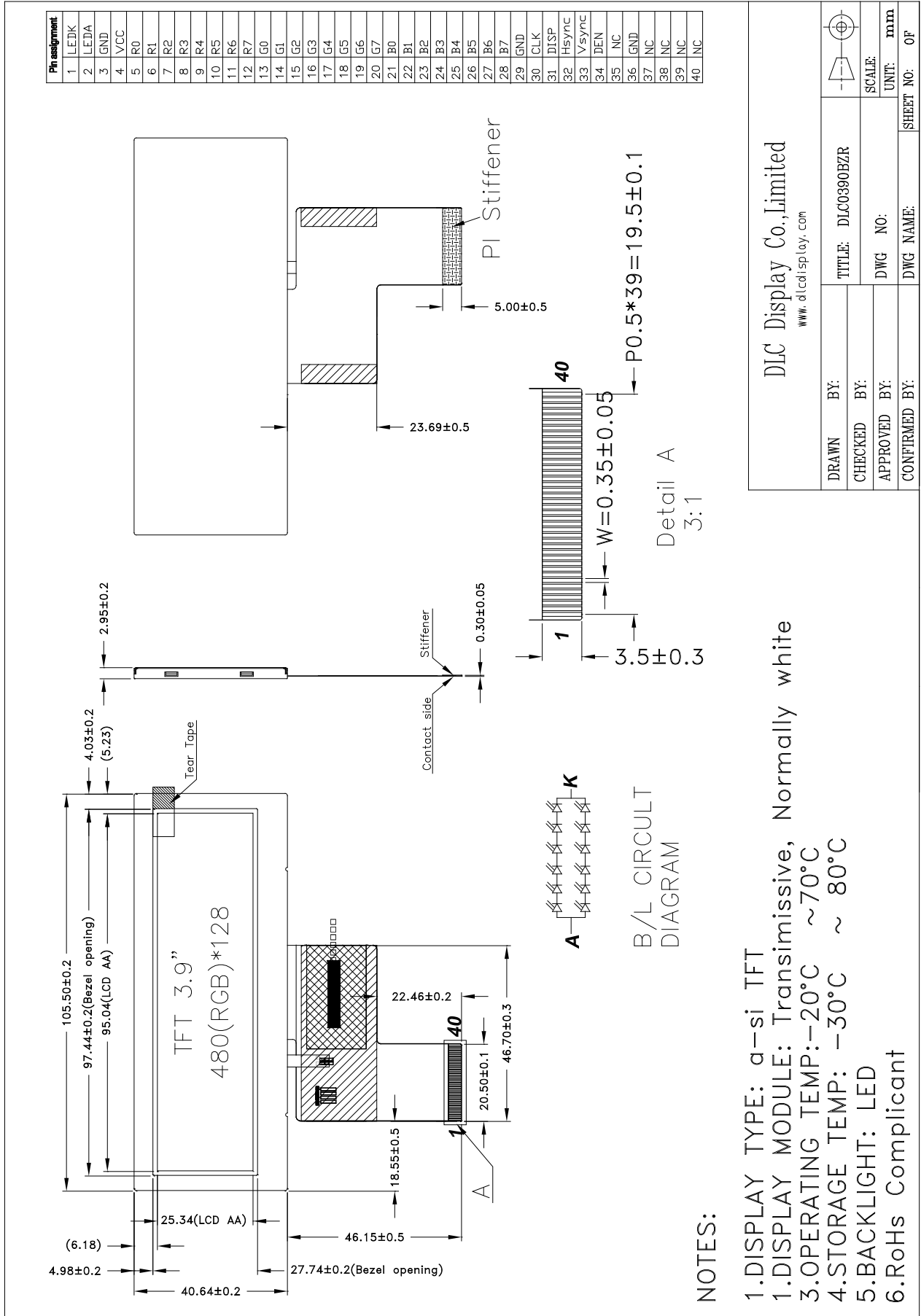
2. Application

Digital equipments which need color display, mobile phone, mobile navigator/video systems.

3. General Information

Item	Contents	Unit
Size	3.9	inch
Resolution	480x(RGB) x 128	/
Interface	24-bit RGB interface	/
Technology type	a-Si TFT	/
Pixel pitch	0.198 x 0.198	mm
Pixel Configuration	R.G.B. Vertical Stripe	
Outline Dimension (W x H x D)	105.5 x 40.64 x 2.95	mm
Active Area	95.04 x 25.34	mm
Display Mode	Transmissive, Normally White	/
Viewing Direction	12 O'clock	/
Backlight Type	LED	/
Driver IC	OTA5180A	/
Weight	28	g

4. Outline Drawing



5. Interface signals

Pin	Symbol	Description	Remark
1	LEDK	Backlight Cathode	
2	LEDA	Backlight Anode	
3	GND	Ground	
4	VCC	Power source	
5	R0	Red data signal	
6	R1	Red data signal	
7	R2	Red data signal	
8	R3	Red data signal	
9	R4	Red data signal	
10	R5	Red data signal	
11	R6	Red data signal	
12	R7	Red data signal	
13	G0	Green data signal	
14	G1	Green data signal	
15	G2	Green data signal	
16	G3	Green data signal	
17	G4	Green data signal	
18	G5	Green data signal	
19	G6	Green data signal	
20	G7	Green data signal	
21	B0	Blue data signal	
22	B1	Blue data signal	
23	B2	Blue data signal	
24	B3	Blue data signal	
25	B4	Blue data signal	
26	B5	Blue data signal	
27	B6	Blue data signal	
28	B7	Blue data signal	
29	GND	Ground	
30	CLK	Clock signal to sample each data	
31	DISP	Display on/off signal DISP="H": Display on DISP="L": Display off	
32	HSYNC	Horizontal synchronizing signal	
33	VSYNC	Vertical synchronizing signal	
34	DEN	Input data enable control.	
35	NC	No connection	
36	GND	Ground	
37~40	NC	No connection	

6. Absolute maximum Ratings

6.1. Electrical Absolute max. ratings

VSS=0V, Ta = 25°C

Parameter	Symbol	MIN	MAX	Unit	Remark
Power Voltage	VCC	-0.3	4.5	V	

6.2. Environment Conditions

VSS=0V, Ta = 25°C

Item	Symbol	MIN	MAX	Unit	Remark
Operating Temperature	TOPR	-20	70	°C	
Storage Temperature	TSTG	-30	80	°C	

Note 1: If Ta below 50°C, the maximal humidity is 90%RH, if Ta over 50°C, absolute humidity should be less than 60%RH.

Note 2: The response time will be extremely slow when the operating temperature is around -10°C, and the back ground will become darker at high temperature operating.

7. Electrical Specifications

7.1 Electrical characteristics

GND=0V, Ta=25°C

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Power Supply Voltage	VCC	3.0	3.3	3.6	V	
Input Logic Voltage	VIL	GND	--	0.3*VCC	V	
	VIH	0.7*VCC	--	VCC	V	
Output Logic Voltage	VOL	GND	--	GND+0.4	V	
	VOH	VCC-0.4	--	VCC	V	
Current Consumption All Black	Logic	ICC+IIN	-	10	20	mA
	Analog					

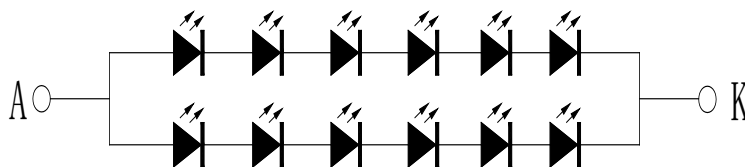
7.2 Backlight Characteristics

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Forward Voltage	VF	17.4	19.2	20.4	V	Ta=25 °C, IF=20mA/LED
Forward Current	IF	--	40	--	mA	Ta=25 °C, IF=20mA/LED
Power dissipation	PD	--	768	--	mW	
LED Life Time(25 °C)		--	30,000	--	Hr	
Drive method	Constant current					
LED Configuration	12 White LEDs (6 LEDs in one string and 2 groups in parallel)					

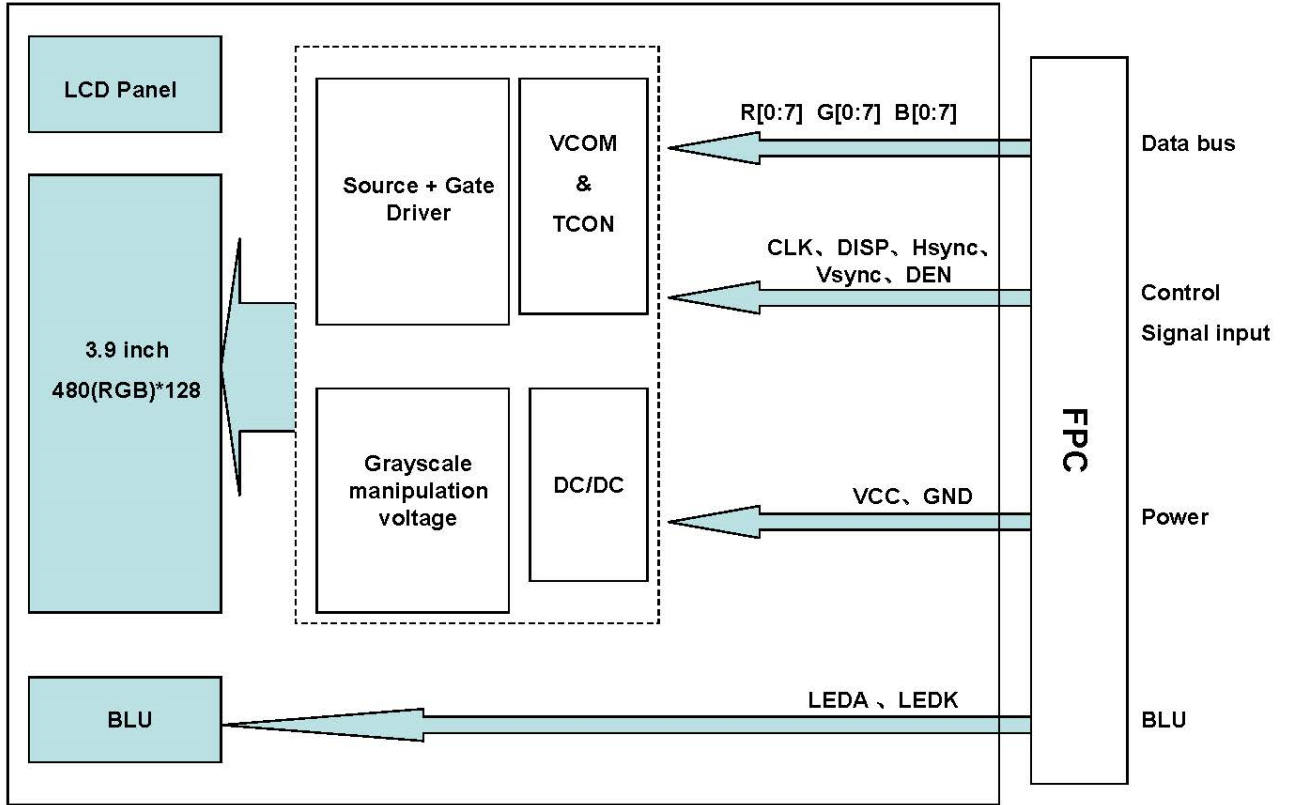
Note1 : LED life time defined as follows: The final brightness is at 70% of original brightness.

(Required constant current supply IF =20mA and
Environmental condition: 25°C ±3°C, 60% ±10%RH)

Note2: Backlighting circuit



7.3 Schematic of LCD module system



8. Command/AC Timing

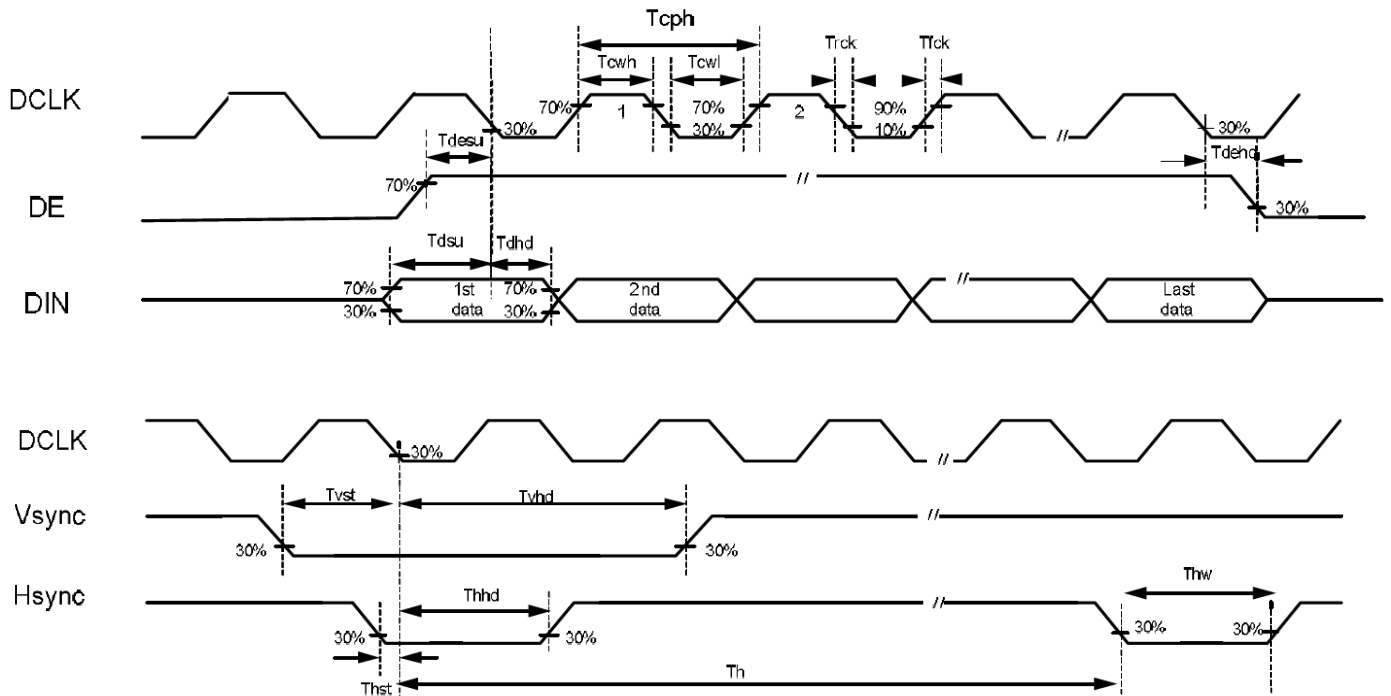
8.1 AC Characteristics

VDDIO=1.8V, VDD=3.3V, AVDD=6V, AGND=0V, TA=-20°C to 80°C

Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
CLK pulse duty	T _{cw}	40	50	60	%	
Hsync width	T _{hw}	1.0	-	-	DCLK	
Hsync period	T _h	55	60	65	us	
Vsync setup time	T _{vst}	12	-	-	ns	
Vsync hold time	T _{vhd}	12	-	-	ns	
Hsync setup time	T _{hst}	12	-	-	ns	
Hsync hold time	T _{hhd}	12	-	-	ns	
Data set-up time	T _{d_{su}}	12	-	-	ns	
Data hold time	T _{d_{hd}}	12	-	-	ns	
DE set-up time	T _{d_{esu}}	12	-	-	ns	
DE hold time	T _{d_{ehd}}	12	-	-	ns	
SD output stable time	T _{st}	-	10	12	ns	
GD output rise and fall time	T _{gst}	-	500	1000	ns	

8.2 AC Timing Diagram

8.2.1 Clock and Data Input Timing Diagram



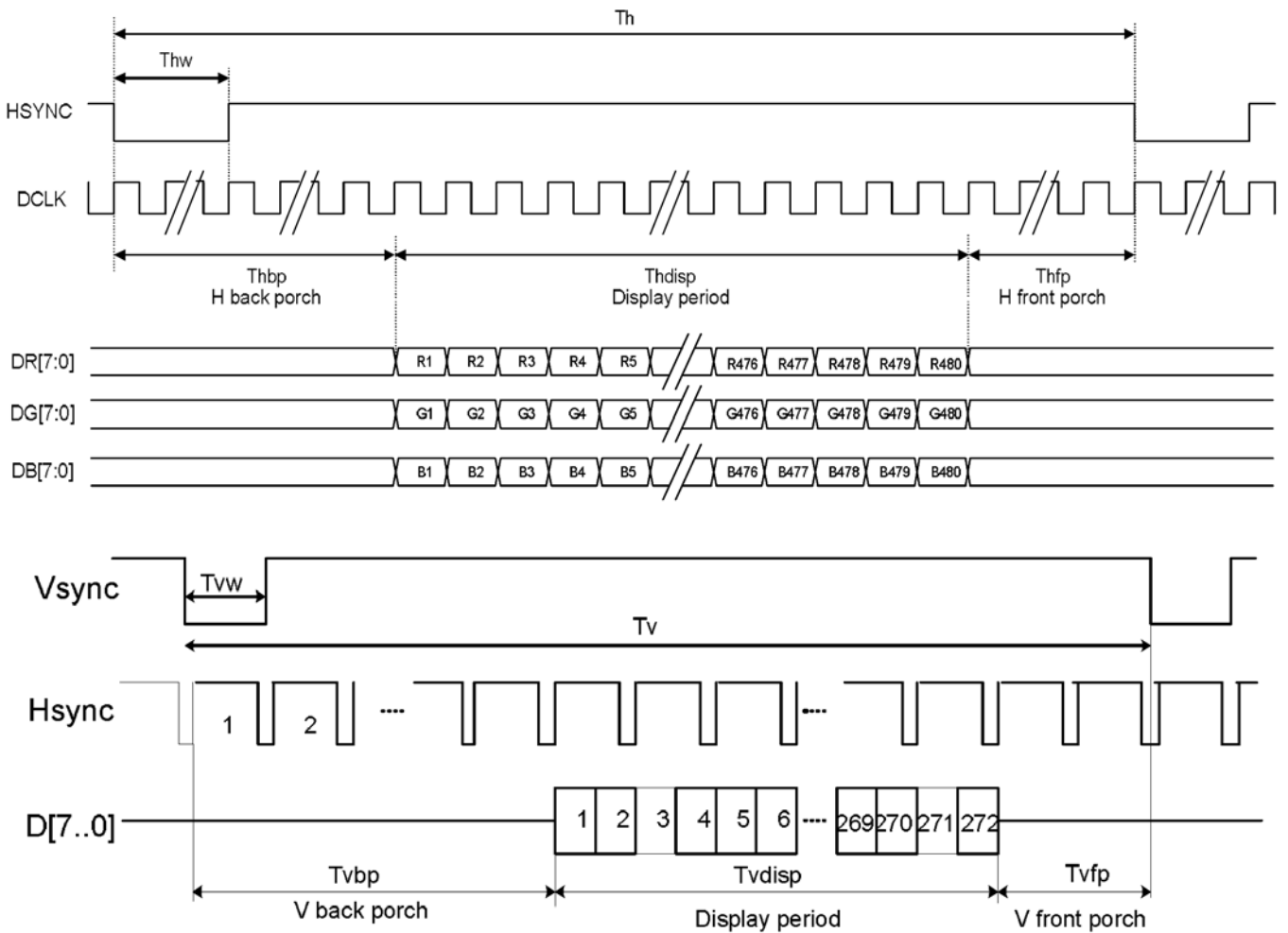
8.3 Input Data Format

8.3.1 Parallel RGB Input Timing Table

Item	System	Min.	Typ.	Max.	Unit	Conditions	
DCLK Frequency	Fclk	-	10.7	-	MHz		
Hsync	Period Time	Th	-	524	-	DCLK	
	Display Period	Thdisp	-	480	-	DCLK	
	Back Porch	Thbp	-	43	-	DCLK	By H_BLANKING setting
	Front Porch	Thfp	-	1	-	DCLK	
	Pulse Width	Thw	-	2	-	DCLK	
Vsync	Period Time	Tv	-	288	-	H	
	Display Period	Tvdisp	-	272	-	H	
	Back Porch	Tvbp	-	12	-	H	By V_BLANKING setting
	Front Porch	Tvfp	-	4	-	H	
	Pulse Width	Tvw	-	2	-	H	

Note: The 1-144 gate lines must be sent black data.

8.3.2 SYNC Mode Timing Diagram



9. Optical Specification

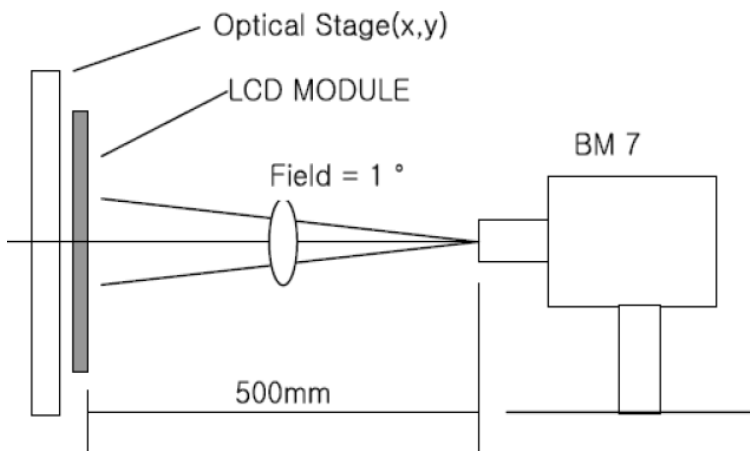
Ta=25°C

Item	Symbol	Condition	Min	Typ.	Max.	Unit	Remark
Contrast Ratio	CR	$\theta=0^\circ$	350	500	-		Note1 Note2
Response Time	Ton/ Toff	25°C	-	30	45	ms	Note1 Note3
View Angles	θT	$CR \geq 10$	55	70	-	Degree	Note 4
	θB		55	70	-		
	θL		55	70	-		
	θR		55	70	-		
Chromaticity	White	Brightness is on	x	0.239	0.289	0.339	Note5, Note1
			y	0.287	0.337	0.387	
	Red		x	0.557	0.607	0.657	
			y	0.292	0.342	0.392	
	Green		x	0.280	0.330	0.380	
			y	0.566	0.616	0.666	
	Blue		x	0.100	0.150	0.200	
			y	0.068	0.118	0.168	
NTSC	S		-	50	-	%	Note5
Luminance	Lv		500	600	-	cd/m ²	

Note 1: Definition of optical measurement system.

Temperature = 25°C(±3°C)

LED back-light: ON, Environment brightness < 150 lx

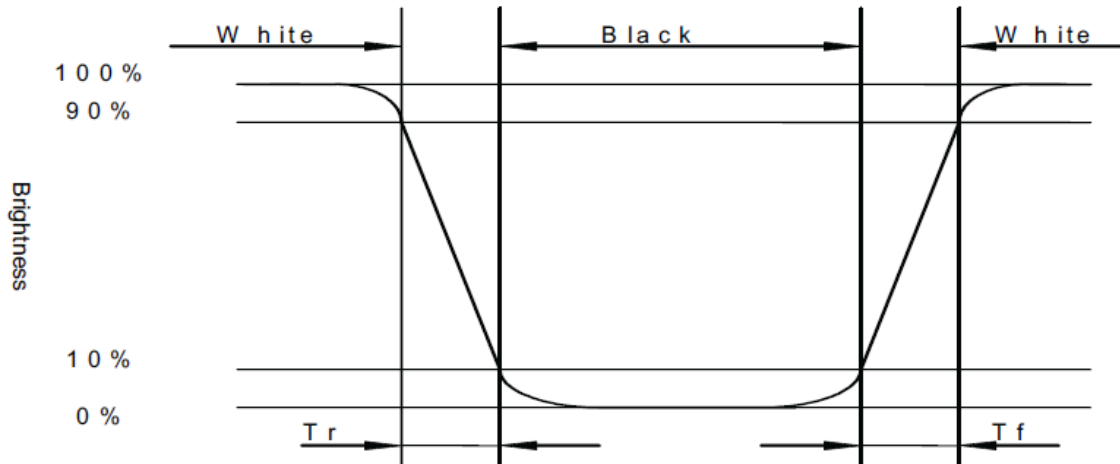


Note 2: Contrast ratio is defined as follow:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

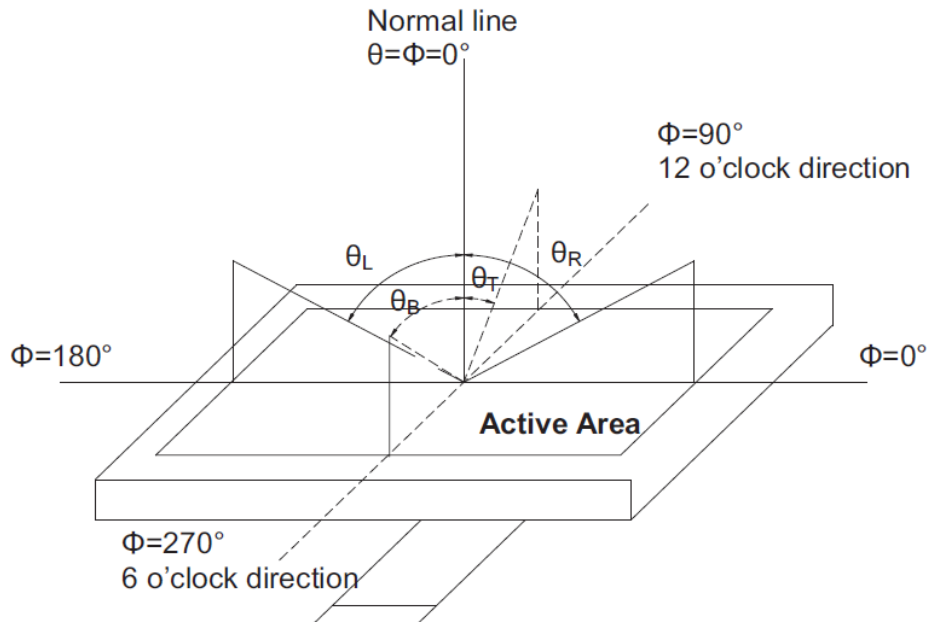
Note 3: Response time is defined as follow:

Response time is the time required for the display to transition from black to white (Rise Time, T_r) and from white to black(Decay Time, T_f).



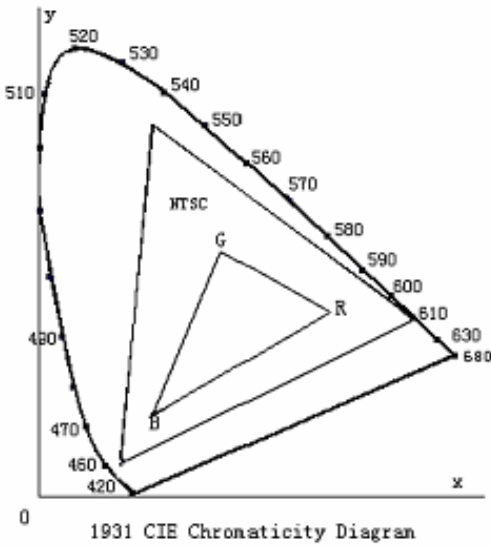
Note 4: Viewing angle range is defined as follow:

Viewing angle is measured at the center point of the LCD.



Note 5: Color chromaticity is defined as follow: (CIE1931)

Color coordinates measured at center point of LCD.



$$S = \frac{\text{area of RGB triangle}}{\text{area of NTSC triangle}} \times 100\%$$

Note 6: Luminance is defined as follow:

Luminance is defined as the brightness of all pixels “White” at the center of display area on optimum contrast.

Note 7: Luminance Uniformity is defined as follow:

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

$$\text{Uniformity}(U) = \frac{\text{Minimum Luminance(brightness) in 9 points}}{\text{Maximum Luminance(brightness) in 9 points}}$$

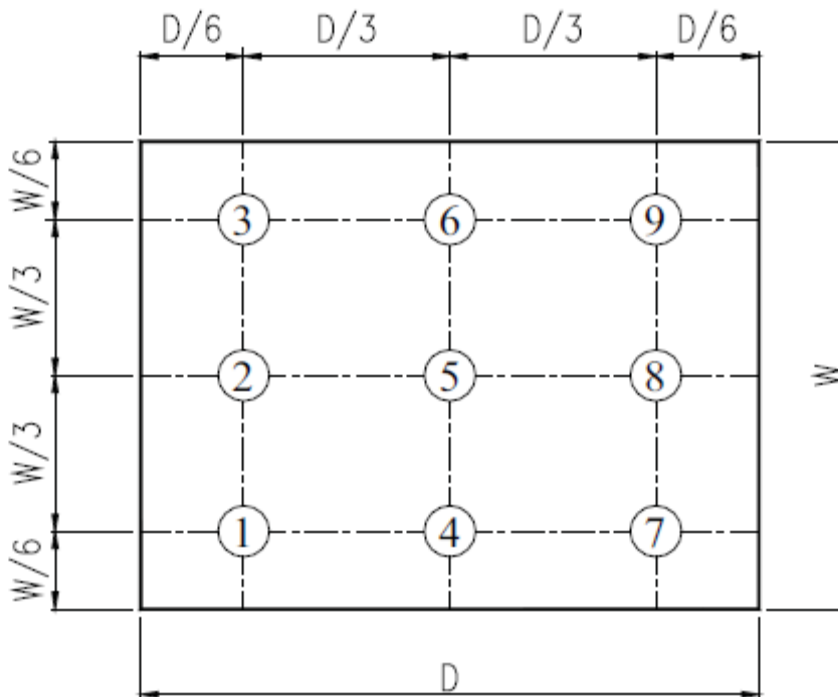


Fig. 2 Definition of uniformity

10. Environmental / Reliability Tests

No	Test Item	Condition	Judgment criteria
1	High Temp Operation	Ts=+70°C, 96hrs	Per table in below
2	Low Temp Operation	Ta=-20°C, 96hrs	Per table in below
3	High Temp Storage	Ta=+80°C, 96hrs	Per table in below
4	Low Temp Storage	Ta=-30°C, 96hrs	Per table in below
5	High Temperature & High Humidity Operation	Ta=+50°C,90%RH,96hours	Per table in below (polarizer discoloration is excluded)
6	Thermal Shock (Non-operation)	-20°C 30 min~+70°C 30 min, Change time:5min, 100 Cycles	Per table in below
7	ESD (Operation)	C=150pF, R=330Ω · 5points/panel Air:±8KV, 5times; Contact:±4KV, 5 times; (Environment:15°C~35°C, 30%~60%.86Kpa~106Kpa)	Per table in below
8	Vibration (Non-operation)	Frequency range:10~55Hz, Amplitude:1 mm Sweep Time:11 mins Test Period: 6 Cycles for each direction of X,Y,Z	Per table in below
9	Shock (Non-operation)	100G 6ms, ±X,±Y,±Z 3times, for each direction	Per table in below

INSPECTION	CRITERION(after test)
Appearance	No Crack on the FPC, on the LCD Panel
Alignment of LCD Panel	No Bubbles in the LCD Panel No other Defects of Alignment in Active area
Electrical current	Within device specifications
Function / Display	No Broken Circuit, No Short Circuit or No Black line No Other Defects of Display

11. Precautions for Use of LCD Modules

11.1 Safety

The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

11.2 Handling

- A. The LCD and touch panel is made of plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
- B. Do not handle the product by holding the flexible pattern portion in order to assure the reliability
- C. Transparency is an important factor for the touch panel. Please wear clear finger sacks, gloves and mask to protect the touch panel from finger print or stain and also hold the portion outside the view area when handling the touch panel.
- D. Provide a space so that the panel does not come into contact with other components.
- E. To protect the product from external force, put a covering lens (acrylic board or similar board) and keep an appropriate gap between them.
- F. Transparent electrodes may be disconnected if the panel is used under environmental conditions where dew condensation occurs.
- G. Property of semiconductor devices may be affected when they are exposed to light, possibly resulting in IC malfunctions.
- H. To prevent such IC malfunctions, your design and mounting layout shall be done in the way that the IC is not exposed to light in actual use.

11.3 Static Electricity

- A. Ground soldering iron tips, tools and testers when they are in operation.
- B. Ground your body when handling the products.
- C. Power on the LCD module before applying the voltage to the input terminals.
- D. Do not apply voltage which exceeds the absolute maximum rating.
- E. Store the products in an anti-electrostatic bag or container.

11.4 Storage

- A. Store the products in a dark place at $+25^{\circ}\text{C} \pm 10^{\circ}\text{C}$ with low humidity (40% RH to 60% RH). Don't expose to sunlight or fluorescent light.
- B. Storage in a clean environment, free from dust, active gas, and solvent.

11.5 Cleaning

- A. Do not wipe the touch panel with dry cloth, as it may cause scratch.
- B. Wipe off the stain on the product by using soft cloth moistened with ethanol. Do not allow ethanol to get in between the upper film and the bottom glass. It may cause peeling issue or defective operation. Do not use any organic solvent or detergent other than ethanol.

11.6 Cautions for installing and assembling

Bezel edge must be positioned in the area between the Active area and View area. The bezel may press the touch screen and cause activation if the edge touches the active area. A gap of approximately 0.5mm is needed between the bezel and the top electrode. It may cause unexpected activation if the gap is too narrow. There is a tolerance of 0.2 to 0.3mm for the outside dimensions of the touch panel and tail. A gap must be made to absorb the tolerance in the case and connector.

