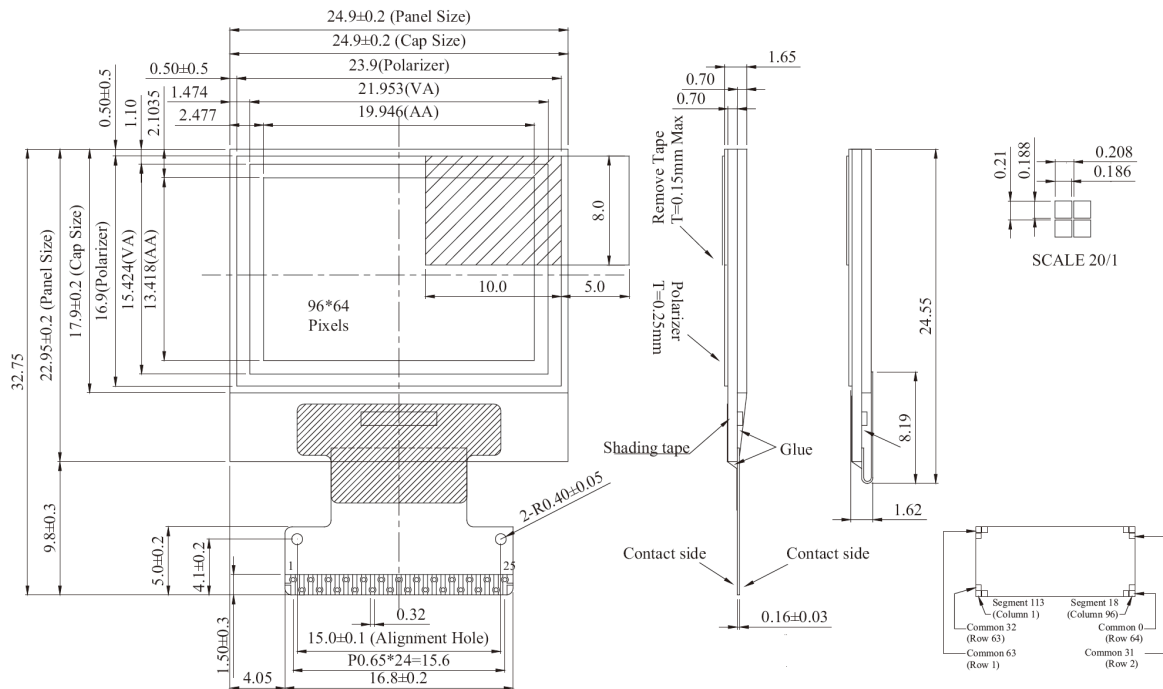


**Dimension drawing**



**Feature**

1. 96x64 dots
2. Built-in Controller SSD1305Z
3. +3V power supply
4. 1/64 duty cycle
5. Interface: 6800, 8080, SPI, I2C
6. Polarizer optional

Pin	NO.	Symbol	Description															
1	N.C. (GND)		Reserved Pin (Supporting Pin)															
2	Vcc		Power Supply for OLED Panel															
3	VSS		Ground of Logic Circuit															
4	VDD		Power Supply for Core Logic Operation															
5	VDDIO		Power Supply for Interface Logic Level															
6	BS1		Communicating Protocol Select															
7	BS2		These pins are MCU interface selection input. See the following table:															
			<table border="1"> <thead> <tr> <th></th> <th>68XX-parallel</th> <th>80XX-parallel</th> <th>Serial</th> <th>I2C</th> </tr> </thead> <tbody> <tr> <td>BS1</td> <td>0</td> <td>1</td> <td>0</td> <td>1</td> </tr> <tr> <td>BS2</td> <td>1</td> <td>1</td> <td>0</td> <td>0</td> </tr> </tbody> </table>		68XX-parallel	80XX-parallel	Serial	I2C	BS1	0	1	0	1	BS2	1	1	0	0
	68XX-parallel	80XX-parallel	Serial	I2C														
BS1	0	1	0	1														
BS2	1	1	0	0														
8	CS#		Chip Select															
9	RES#		Power Reset for Controller and Driver															
10	D/C#		Data/Command Control															
11	R/W#		Read/Write Select or Write															
12	E/RD#		Read/Write Enable or Read															
13	D0		Host Data Input/Output Bus															
14	D1																	
15	D2																	
16	D3																	
17	D4																	
18	D5																	
19	D6																	
20	D7																	
21	IREF		Current Reference for Brightness Adjustment															
22	VCOMH		Voltage Output High Level for COM Signal															
23	VCC		Power Supply for OEL Panel															
24	VLSS		Ground of Analog Circuit															
25	N.C. (GND)		Reserved Pin (Supporting Pin)															

**Mechanical Date**

Item	Dimension	Unit
Module dimension	24.9 × 22.95 × 1.65	mm
View area	21.953 × 15.424	mm
Active area	19.946 × 13.418	mm
Dot size	0.186 × 0.186	mm
Dot pitch	0.208 × 0.21	mm

**Absolute Maximum Rating**

Parameter	Symbol	Min	Max	Unit	Notes
Supply Voltage for Logic	V <sub>DD</sub>	-0.3	4	V	1, 2
Supply Voltage for I/O Pins	V <sub>DDIO</sub>	-0.3	V <sub>DD</sub> +0.5	V	1, 2
Supply Voltage for Display	V <sub>CC</sub>	0	15	V	1, 2

**Electronical Characteristics**

Characteristics	Symbol	Conditions	Min	Typ	Max	Unit
Supply Voltage for Logic	V <sub>DD</sub>		2.4	2.8	3.5	V
Supply Voltage for I/O Pins	V <sub>DDIO</sub>		1.6	1.8	V <sub>DD</sub>	V
Supply Voltage for Display	V <sub>CC</sub>	Note 3	10.5	11	11.5	V
High Level Input	V <sub>IH</sub>	I <sub>OUT</sub> = 100μA, 3.3MHz	0.8×V <sub>DDIO</sub>	-	V <sub>DDIO</sub>	V
Low Level Input	V <sub>IL</sub>	I <sub>OUT</sub> = 100μA, 3.3MHz	0	-	0.2×V <sub>DDIO</sub>	V
High Level Output	V <sub>OH</sub>	I <sub>OUT</sub> = 100μA, 3.3MHz	0.9×V <sub>DDIO</sub>	-	V <sub>DDIO</sub>	V
Low Level Output	V <sub>OL</sub>	I <sub>OUT</sub> = 100μA, 3.3MHz	0	-	0.1×V <sub>DDIO</sub>	V
Operating Current for V <sub>DD</sub>	I <sub>DD</sub>	Note 4	--	180	300	μA
Operating Current for V <sub>CC</sub>	I <sub>CC</sub>	Note 4	--	180	5.3	mA
Sleep Mode Current for V <sub>DD</sub>	I <sub>DD, SLEEP</sub>	Note 5	-	1	5	μA
Sleep Mode Current for V <sub>CC</sub>	I <sub>CC, SLEEP</sub>		-	1	5	μA

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